		STUDY MODULE D	ESCRIPTION FORM			
Name o	f the module/subject	Code 1010322331010323332				
Field of	study	is in clear and agricult dev	Profile of study	Year /Semester		
Electrical Engineering			(general academic, practical) (brak)	2/3		
Elective path/specialty			Subject offered in:	Course (compulsory, elective)		
Cvcle o	study:		Form of study (full-time.part-time)	obligatory		
	Second-c	ycle studies	full-time			
No. of h	ours			No. of credits		
Lecture: 15 Classes: - Laboratory: -			Project/seminars:	30 6		
Status o	of the course in the study	program (Basic, major, other)	(university-wide, from another field)			
(brak)			(brak)			
Educati	on areas and fields of sci	ence and art		ECTS distribution (number and %)		
techr	nical sciences			6 100%		
	Technical scie	ences		6 100%		
Resp	onsible for subje	ect / lecturer:	Responsible for subject	ct / lecturer:		
Pro	. dr hab. inż. Lech No	wak	Dr inż. Krzysztof Kowalski			
ema tel	ail: lech.nowak@put.po 61 665 2380	oznan.pl	email: krzysztof.kowalski@	put.poznan.pl		
Wyo	dział Elektryczny		Wydział Elektryczny			
ul. F	Piotrowo 3A, 60-965 P	oznań	ul. Piotrowo 3Á, 60-965 Poznań			
Prere	quisites in term	s of knowledge, skills an	d social competencies:			
1	Knowledge	Basic knowledge of mathematic	cal analysis, linear algebra and vectorial calculus.			
2	Skills	Programming ability on the basi effective self-education in the fie	on the basic level. Formulating the design problem. Ability of the on in the field associated with chosen subject.			
3	Social competencies	Student is aware of a need to ex in the team.	kpand its competence, readines	s to undertake the cooperation		
Assu	mptions and obj	ectives of the course:				
Getting about consid selection	the ability of correct f deterministic and non- ering. Ability of the ide on of the algorithm of t	ormulating the problem of synthe deterministic methods of the unco entification and formulating tasks of the optimization to the solved the	sis and optimization of technica onstrained optimization; getting of the multi-criteria optimization. put problem.	I device. Getting the knowledge to know methods of constrains Purchasing the ability of the		
	Study outco	mes and reference to the	educational results for	a field of study		
Knov	vledge:					
1. Stuc applied	lent has an expanded I mathematics, essen	and deepened knowledge in som tial for description of operation ar	e branches of mathematics, inc nd optimum synthesis of electric	cluding elements of discreet and cal systems [K_W01 ++]		
2. Stud issues	lent has an expanded in electrical engineeri	knowledge in the scope of advan ng - [K_W02 +++]	ced numerical methods applied	for solving of complex technical		
3. Stuc [K_W1	lent has a knowledge 8 ++]	in the possibility and restrictions c	of methods used in CAD in the a	area of electrical engineering -		
Skills	:					
1. Stuc informa	lent is able to obtain ir ation, to effect their int	nformation from literature, database erpretation - [K_U01 +]	ses and other sources; he is abl	e to integrate obtained		
2. Stuc [K_U0	lent is able to use met δ ++]	hods and mathematical models for	or analysis and designing electr	ical devices and systems -		
3. Student is able to design electrical elements, devices and systems, including set functional and economic criteria, in case of the need adapting existing or developing new CAD tools [K_U12 +++]						
Socia	al competencies:					
1. The achiev	student understands tements in the area of	the need of formulating both hand electrical engineering and other a	ing over to the society informati spects of activity of an electrica	ion and opinions of I engineer - [K K02 ++]		

Assessment methods of study outcomes

Lecture:

?

constant judging on every classes (awarding a bonus to the activity and qualities of the perception),

? evaluation of the knowledge and abilities on an examination.

Design classes-seminar:

? the test and awarding a bonus to the essential knowledge for stated implementations of problems in the given area of theoretical tasks,

? constant judging, on every classes - awarding a bonus to the increase in the ability of using with found principles and methods.

Getting additional points for the activity during classes, particularly for:

- ? proposing discussing additional aspects of the issue,
- ? effectiveness of applying the acquired knowledge while solving a set problem,
- ? Remarks about improving teaching materials.
- ? drawing up individual test and design tasks

Course description

Electromagnetic device synthesis, formulation of the device optimization problem: decision variables, objective function, constrain functions. Normalization of variables and functions. Deterministic method of unconstrained optimization. The gradient procedures, conjugate gradient algorithms. Algorithms of direction optimization.

Evolutionary methods: genetic algorithm, particle swarm procedure. Equality constrained optimization, Lagrange multipliers and Courrant procedure. Inequality constrained methods: external and internal penalty functions. Multi-criteria optimization.

Basic bibliography:

1. Podstawy optymalizacji, A. Stachurski, A. Wierzbicki, Oficyna Wydawnicza Politechniki Warszawskiej, Warszawa 2001

2. Optymalizacja, Wybrane metody z przykładami zastosowań, J. Kusiak, A. Danielewska-Tułecka, P. Oprocha, PWN, Warszawa 2009

3. Teoria i metody obliczeniowe optymalizacji, Findeisen W., Szymanowski J., Wierzbicki A., Państwowe Wydawnictwo Naukowe, Warszawa, 1977

4. Algorytmy genetyczne i ich zastosowania, D.E. Gloldberg, WNTWarszawa, 1998

Additional bibliography:

1. Global optimization, Torn A., Zilinskas A., Springer Verlag, Berlin, 1987

2. Wykłady z Modelowania Matematycznego, Wybrane algorytmy optymalizacji, Algorytmy genetyczne, Algorytmy mrówkowe

R. Grzymkowski, K. Kaczmarek, St. Kiełtyka, I. Nowak, Pracownia Komputerowa Jacka Skalmierskiego Gliwice 2008.

3. Genetic algorithms in search, optimization and machine learning, Goldberg E.D., Addison Wesley Publishing Company, Inc., 1989

Result of average student's workload

Activity	Time (working hours)
1. Participation in the lecture	15
2. Participation in the seminar classes	30
3. Preparation to the seminar	30
4. Accomplishment of design tasks after behind the laboratory	30
5. Participation in the consultation	15
6. Preparation for examination	30
7. Participation in the examination	5

Student's workload

Source of workload	hours	ECTS
Total workload	155	6
Contact hours	80	3
Practical activities	80	3